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Applicant: Cho, et al. **Docket No:** TI-36298

Serial No.: 10/786,966 Art Unit: 2829

Filed: February 25, 2004 Examiner: Unknown

For: Built-In Self Test Method and Apparatus for Jitter Transfer, Jitter Tolerance, and FIFO Data Buffer

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cho, *et al.* Docket No.: TI-36298
Serial No.: 10/786,966 Art Unit: 2829
Filed: February 25, 2004 Examiner: Unknown
Title: Built-In Self Test Method and Apparatus for Jitter Transfer, Jitter Tolerance, and FIFO Data Buffer

Office of Initial Patent Examination
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REQUEST FOR CORRECTION

Sir:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application. Enclosed is a copy of the Filing Receipt with the changes noted thereon as well as a copy of the signed Declaration showing the correct name of Mr. Bhakta.

Respectfully submitted,



Ira S. Matsil
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Attorney for Applicant

June 8, 2004

Date

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/786,966	02/25/2004	2829	2130	TI-36298	7	43	14

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TEXAS INSTRUMENTS INCORPORATED
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CONFIRMATION NO. 4127
FILING RECEIPT

OC000000012696184

Date Mailed: 05/18/2004

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Applicant(s)

James B. Cho, Plano, TX;
Bhakta-Bhavesh, Richardson, TX;
Bhavesh G. Bhakta

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 05/17/2004

Projected Publication Date: 08/25/2005

Non-Publication Request: No

Early Publication Request: No

Title

Built-in self test method and apparatus for jitter transfer, jitter tolerance, and FIFO data buffer

Preliminary Class

324

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PATENT DEPT

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